

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1

of

1

**Complete if Known**

Application Number	Not yet assigned
Filing Date	20 October 2003
First Named Inventor	Mark Beaumont
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	DB001077-000

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
TM ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓		US- 6,430,618 B1	08-06-2002	Karger et al.	
		US- 6,421,772 B1	07-16-2002	Maeda et al.	
		US- 6,404,439 B1	06-11-2002	Coulombe et al.	
		US- 6,292,822 B1	09-18-2001	Hardwick	
		US- 6,219,776 B1	04-17-2001	Pechanek et al.	
		US- 6,279,088 B1	08-21-2001	Elliot et al.	
		US- 6,078,945	06-20-2000	Hinsley	
		US- 5,966,528	10-12-1999	Wilkinson et al.	
		US- 5,892,517	04-06-1999	Rich	
		US- 5,850,489	12-15-1998	Rich	
		US- 5,630,129	05-13-1997	Wheat	
		US- 5,581,773	12-03-1996	Glover	
		US- 5,535,387	07-09-1996	Matsouka et al.	
		US- 5,241,677	08-31-1993	Naganuma et al.	
		US-			
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

Examiner  
Signature

/Tan Mai/

Date  
Considered

11/22/2006

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language translation is attached.

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				Attorney Docket Number	DB001077-000

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
TM		Daehyun Kim, Mainak Chaudhuri, and Mark Heinrich, Leveraging Cache Coherence in Active Memory Systems, Proceedings of the 16th ACM	
TM		Int'l Conference on Supercomputing, pages 2-13, New York City, USA, June 2002.	
TM		Mainak Chaudhuri, Daehyun Kim, and Mark Heinrich, Cache Coherence Protocol Design for Active Memory Systems, Proceedings of the 2002	
TM		Int'l Conference on Parallel and Distributed Processing Techniques and Applications, pages 83-89, Las Vegas, USA, June 2002.	

Examiner Signature	/Tan Mai/	Date Considered	11/22/2006
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